

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 11-18-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Methods of Routing Programmable Logic Devices to Minimize Programming Time
Attorney Docket Number:: X-1386 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 06
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Jay
Middle Name:: T.
Family Name:: Young
Street:: 359 Troon Court
City:: Louisville
State or Province:: Colorado
Postal or Zip Code:: 80027

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Jeffrey
Middle Name:: V.
Family Name:: Lindholm
Street:: 1008 Sparrow Hawk Drive

City:: Longmont
State or Province:: Colorado
Postal or Zip Code:: 80501

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Sridhar
Family Name:: Krishnamurthy
Street:: 4321 Verdigris Circle
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95134

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124